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TC 1700



Docket No.: 205386US2

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231



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RE: Application Serial No.: 09/820,846

Applicants: Ko ONODERA, et al.

Filing Date: March 30, 2001

For: TERMINAL ELECTRODE FORMING METHOD IN  
CHIP-STYLE ELECTRONIC COMPONENT AND  
APPARATUS THEREFOR

Group Art Unit: 1734

Examiner: LAMB, B

SIR

Attached hereto for filing are the following papers:

**RESPONSE TO RESTRICTION REQUIREMENT**

Our check in the amount of 0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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